

# Level 2

- Classical architecture (3.1)
- CISC architectures
  - IBM 360/370 (3.2)
  - DEC pdp -11 (3.3)
  - VAX (3.4)
  - CDC 6600 (3.6)
- RISC architecture (3.5)

# Phbreak

## MIPS Instruction Set Processor Architecture

- PH Chapter 3, main points:
  - instruction representation
  - instruction types for
    - decisions
    - procedure support
    - miscellany
    - arrays & pointers
  - 2 examples (Sec 3.9, 3.10)

# The Classical machine ( pre - 1964)

- One parameter only : word size
- all data operands occupy 1 word
- all instructions occupy 1 word

# Instruction format



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3

3

15

# Examples

- GE 625
- GE 635
- GE 645
- HIS 6000
  
- IBM 709/7090/7094
- CDC 1604/3600/6400/CYBER